

## **AMENDMENTS TO THE SPECIFICATION**

Please amend the specification as follows:

[0038] Referring now to FIG. 5, the transceiver 500 includes a processor 502 having circuitry to execute instructions. A communications interface 520 is coupled to the processor 502 and collectively includes a first MEMS based delay line 530, a delay/amplitude echo map block 540, a second MEMS delay line 550, a plurality of programmable amplifiers ~~554~~ 552, a power amplifier 560 and an antenna 570. Accordingly, the transceiver 500 utilizes a first MEMS based delay line ~~530~~ 520 and a delay/amplitude echo map block ~~540~~ 530 in order to sample a pulse signal received from a communicating transmission station (not shown).